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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i> Mail Stop: AMENDMENT Express Mail Receipt No. Total Number of Pages in This Submission	Application / Conf. No.	10/696,226 / 5260
	Filing Date	October 28, 2003
	First Named Inventor	Andy T. Nguyen
	Examiner Name	Unknown
	Art Unit	2816
	Patent No.	
Attorney Docket Number		X-1464-1P-1P US

ENCLOSURES (check all that apply)		
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Date	August 27, 2004	Charge any additional fees required/credit any overpayment to our Deposit Account Number: 24-0040

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PATENT

IN THE UNITED STATES PATENT OFFICE

Applicant: Andy T. Nguyen
Assignee: Xilinx, Inc.
Title: "Counter-Based Phased Clock Generator Circuits and Methods"
Serial No.: 10/696,226 File Date: 10-28-03
Examiner: Unknown Art Unit: 2816
Docket No.: X-1464-1P-1P US Conf. No.: 5260

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the three (3) references listed in the attached Substitute for Form 1449A/PTO.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above document shall not be construed as an admission that the document is necessarily prior art with respect to the instant invention. Citation of the above document shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above document shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

Lois D. Cartier
Agent for Applicant
Reg. No. 40,941

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Pat Slaback
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